

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,784	06/24/2003	So-Haeng Cho	8733.850.00	9532
7590 11/15/2004			EXAMINER	
SONG K. JUNG			DUDEK, JAMES A	
McKenna Long & Aldridge LLP				
1900 K Street, NW			ART UNIT	PAPER NUMBER
Washington, DC 20006			2871	
			•	

DATE MAILED: 11/15/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/601,784	CHO ET AL.				
Office Action Summary	Examiner	Art Unit				
	James A. Dudek	2871				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on	_•					
2a) ☐ This action is FINAL . 2b) ☒ This	☐ This action is FINAL. 2b)☑ This action is non-final.					
3) Since this application is in condition for allowan	☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>1-33</u> is/are pending in the application.						
4a) Of the above claim(s) <u>1-9</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>10-33</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s)	_					
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date						
☑ Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date ☑ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 5) ☐ Notice of Informal Patent Application (PTO-152) Paper No(s)/Mail Date 6) ☐ Other:						

Application/Control Number: 10/601,784

Art Unit: 2871

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 11-15 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. It is unclear if the passivation layer in claim 11 is the same passivation claimed in claim 10. The examiner assumes them to be the same passivation layer and thus suggests using "the passivation layer".

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 10, 16-17 and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by US Patent 5886761.

Per claim 10, 761 teaches a method for fabricating a liquid crystal display device, comprising: providing a first substrate [1]; forming a gate electrode and a gate line on the substrate [2]; forming a semiconductor layer pattern on the gate electrode [5]; forming source/drain electrode and a data line substantially perpendicular to the gate line and crossing the gate line to define a pixel region [4]; forming a striped passivation layer on the data line and on the thin film transistor [9]; providing a second substrate facing the first substrate [14]; and providing liquid crystal between the first and second substrate [17].

Per claim 16, 761 teaches the method of claim 10, further comprising: forming a pixel electrode contacting the drain electrode in the pixel region [7].

Per claim 17, 761 teaches the method of claim 16, wherein a portion of the pixel electrode is formed on the drain electrode not covered by the striped passivation film [see figure 14, element 7b]

Application/Control Number: 10/601,784

Art Unit: 2871

Per claim 23, 761 teaches the method of claim 10, wherein the passivation layer has a constant width [see the figures].

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 18-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over 761.

761 lacks a storage electrode over the gate line when the source/drain electrode is formed; a storage line substantially parallel with the gate line in the pixel region; wherein the storage line is formed at the same time that the gate line is formed; the storage line is formed substantially parallel to and adjacent to the data line in the pixel region; and wherein the storage line is formed at the same time that the data line is formed. However, each of these configuration was well known for improving resolution and reducing cross-talk and decreasing the number of method steps required to make the cell. Accordingly, it would have been obvious to one of ordinary skill at the time of invention combine the well known storage electrode and 761.

Claims 11-15 and 24-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over 761 in view of EP 471 626 ("626").

Per claim 11-15, 24-27 and 32-33, 761 lacks the step of cliché printing, rolling and etching. However, 626 teaches such a method and materials for improving efficiency of the cell

Application/Control Number: 10/601,784

Art Unit: 2871

manufacturing process. Accordingly, it would have been obvious to one of ordinary skill at the time of invention combine the method of 626 with the method of 761.

Page 4

Claims 28-31 are rejected under 35 U.S.C. 103(a) as being unpatentable over 761. 761 lacks a storage electrode over the gate line when the source/drain electrode is formed; a storage line substantially parallel with the gate line in the pixel region; wherein the storage line is formed at the same time that the gate line is formed; the storage line is formed substantially parallel to and adjacent to the data line in the pixel region; and wherein the storage line is formed at the same time that the data line is formed. However, each of these configuration was well known for improving resolution and reducing cross-talk and decreasing the number of method steps required to make the cell. Accordingly, it would have been obvious to one of ordinary skill at the time of invention combine the well known storage electrode and 761.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James A. Dudek whose telephone number is 571-272-2290. The examiner can normally be reached on 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H. Kim can be reached on 571-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James A. Dudek Primary Examiner Art Unit 2871